State 0:

Assert pcstall, deassert execute signals (reset state). Dispatch master if possible. If so, dispatch slave if possible. If slave went, go to 2. If slave didn't go, due to a forwarding lock, go to 1. If slave didn't go because it is one of the instructions that the secondary pipeline doesn't support, go to 3.

State 1:

Spin until instruction is dispatched, then goto 2

State 2:

Deassert pcstall

State 3:

Kick slave instruction from secondary pipeline to primary pipeline and dispatch it when ready. Goto 2 when done.