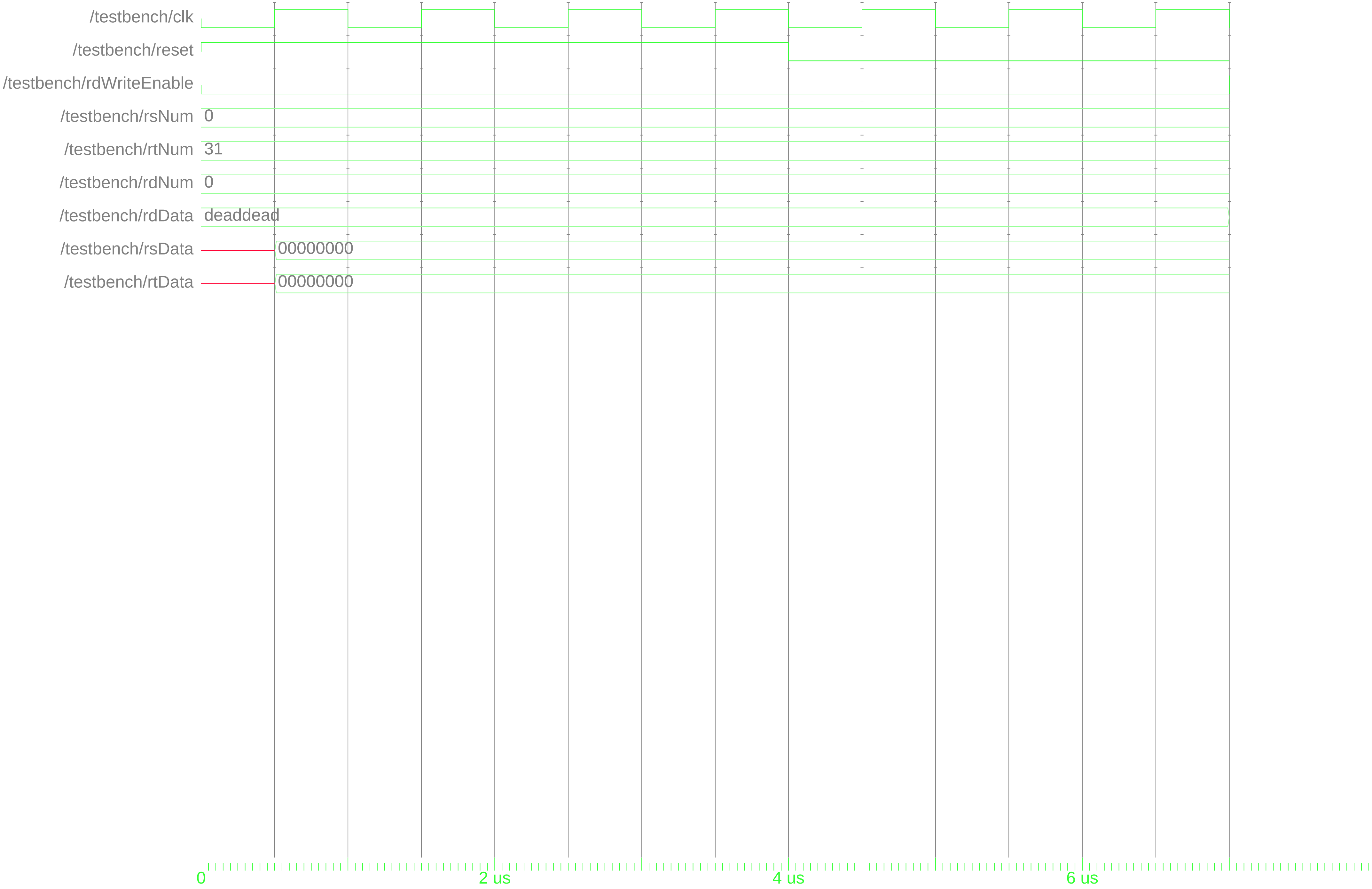
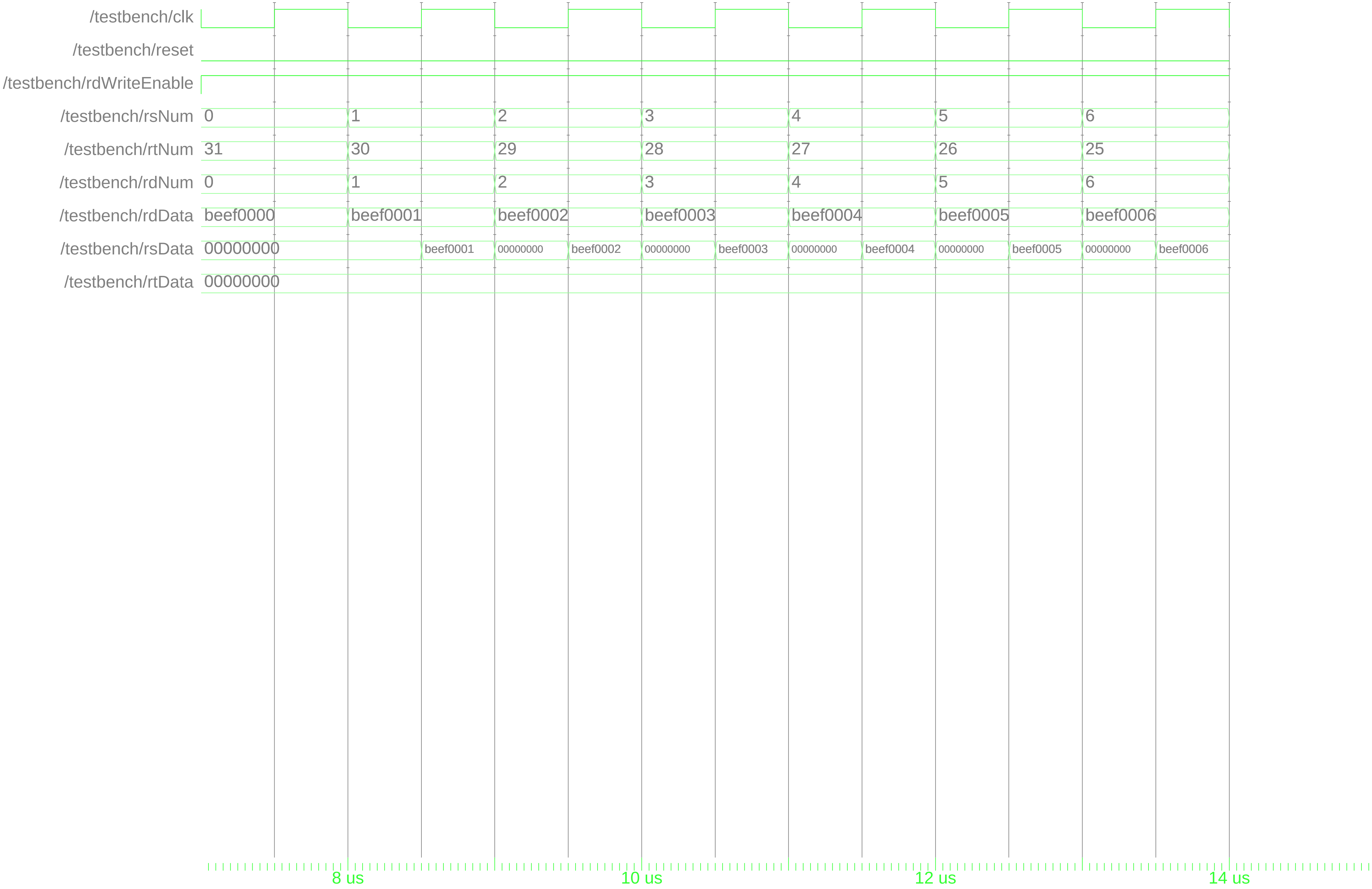
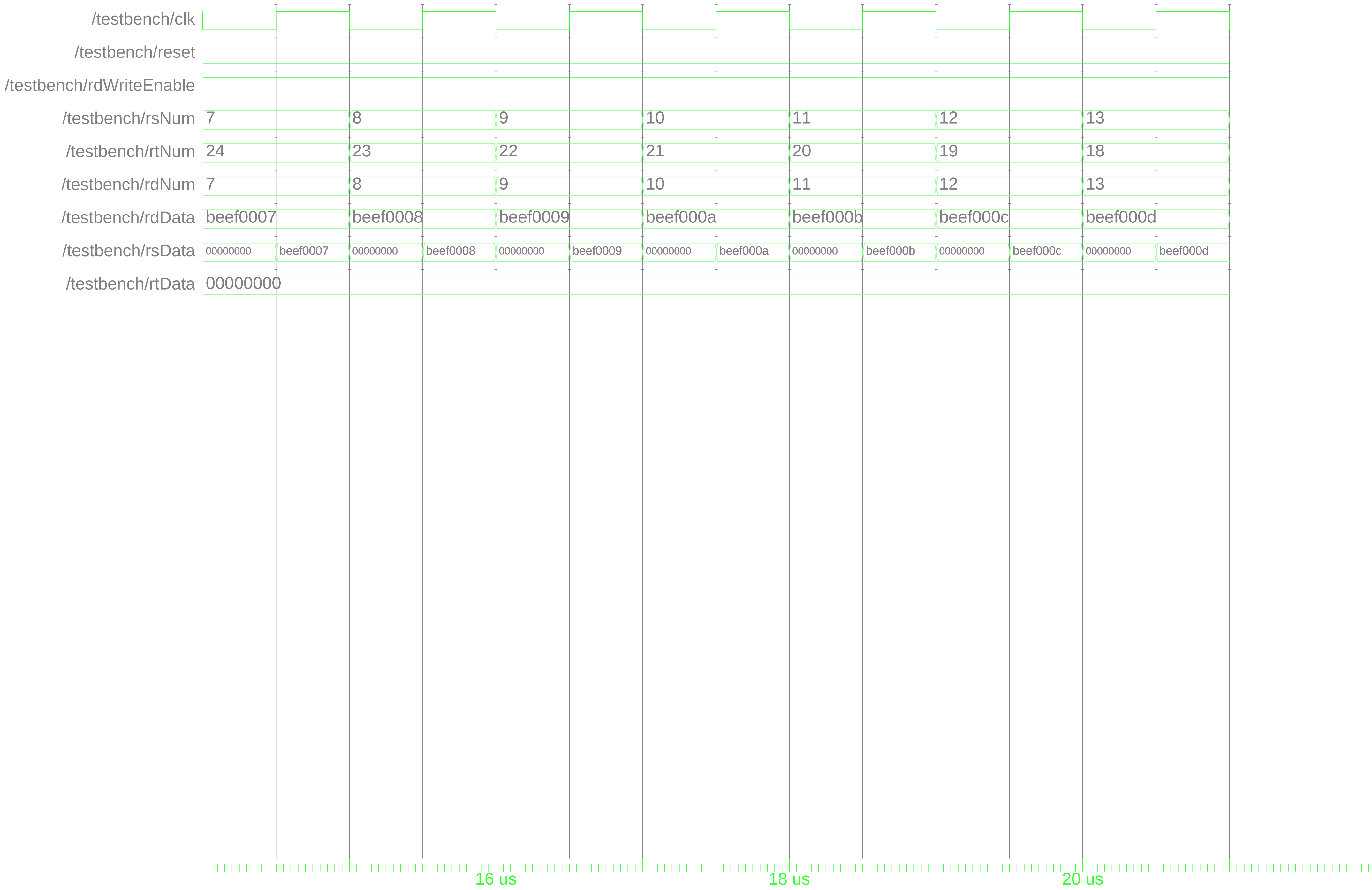
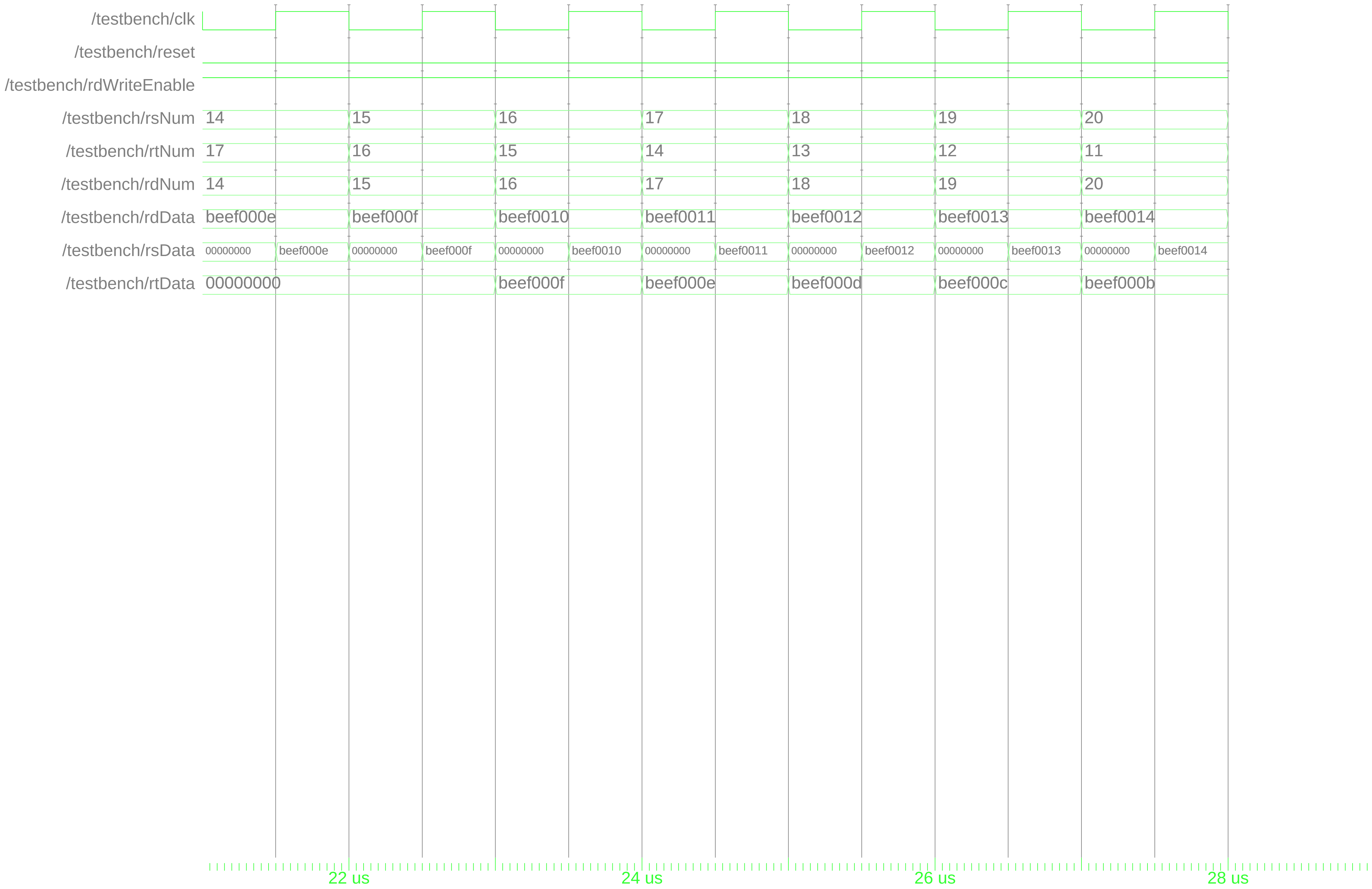


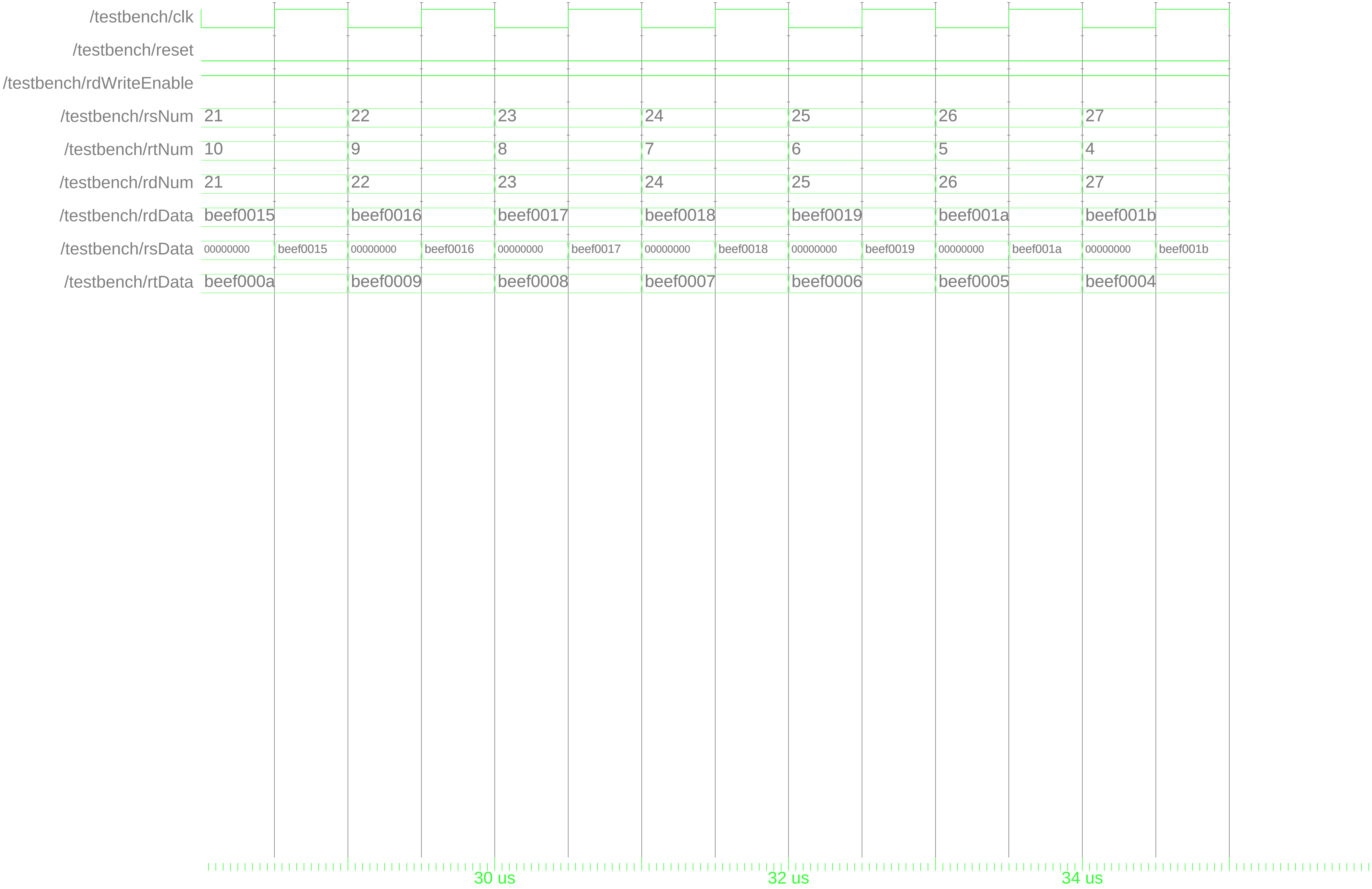
Entity:testbench Architecture: Date: Sun Sep 14 16:48:46 EDT 2003 Row: 1 Page: 1


/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
/testbench/rtNum
/testbench/rdNum
/testbench/rdData
/testbench/rsData
/testbench/rtData
0 1 2 3 4 5 6
31 30 29 28 27 26 25
0 1 2 3 4 5 6
beef0000 beef0001 beef0002 beef0003 beef0004 beef0005 beef0006
00000000 beef0001 00000000 beef0002 00000000 beef0003 00000000 beef0004 00000000 beef0005 00000000 beef0006
00000000
8 us
10 us
12 us
14 us

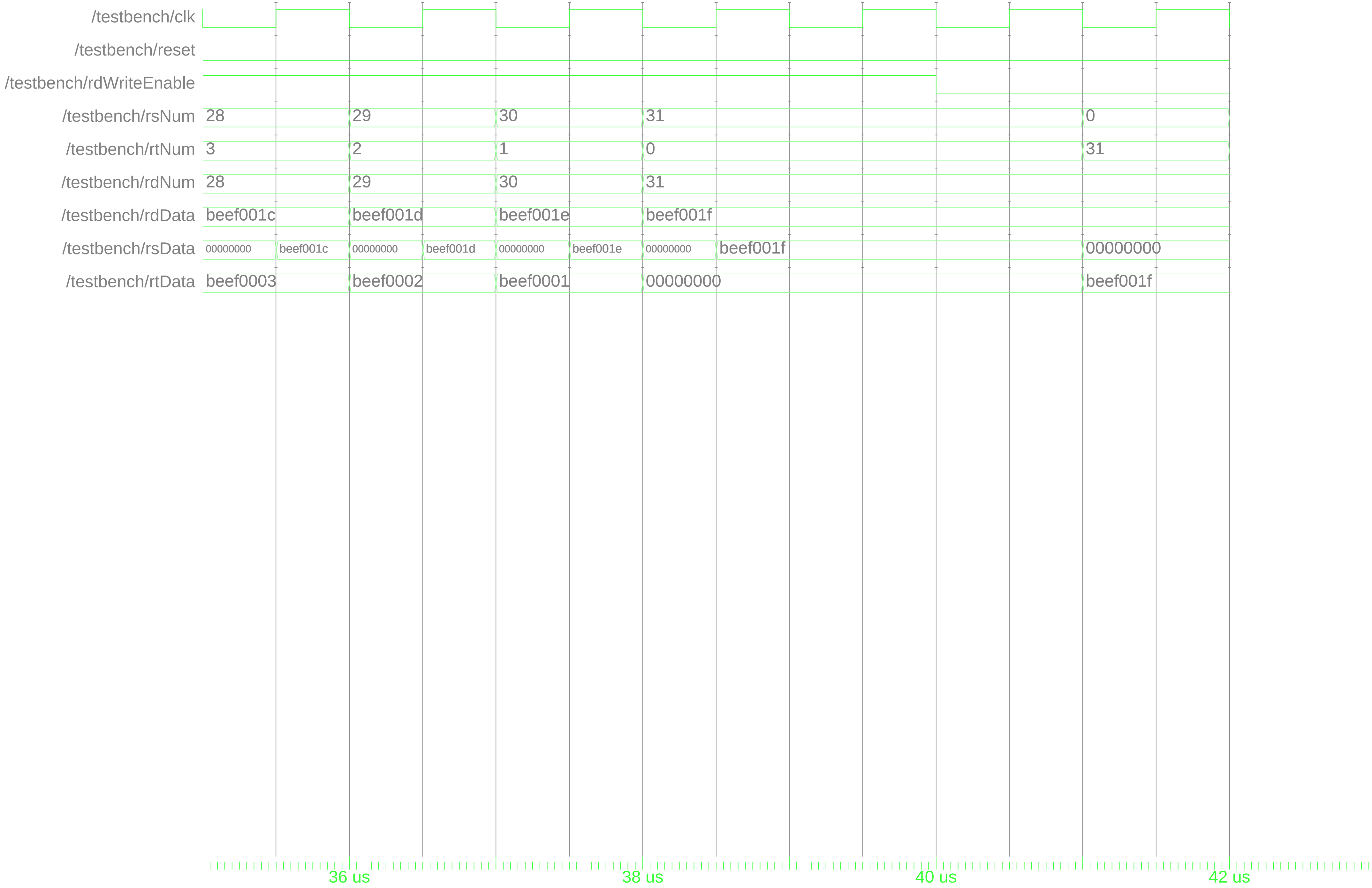

/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
/testbench/rtNum
/testbench/rdNum
/testbench/rdData
/testbench/rsData
/testbench/rtData
7
8
9
10
11
12
13
24
23
22
21
20
19
18
beef0007
beef0008
beef0009
beef000a
beef000b
beef000c
beef000d
00000000
16 us
18 us
20 us



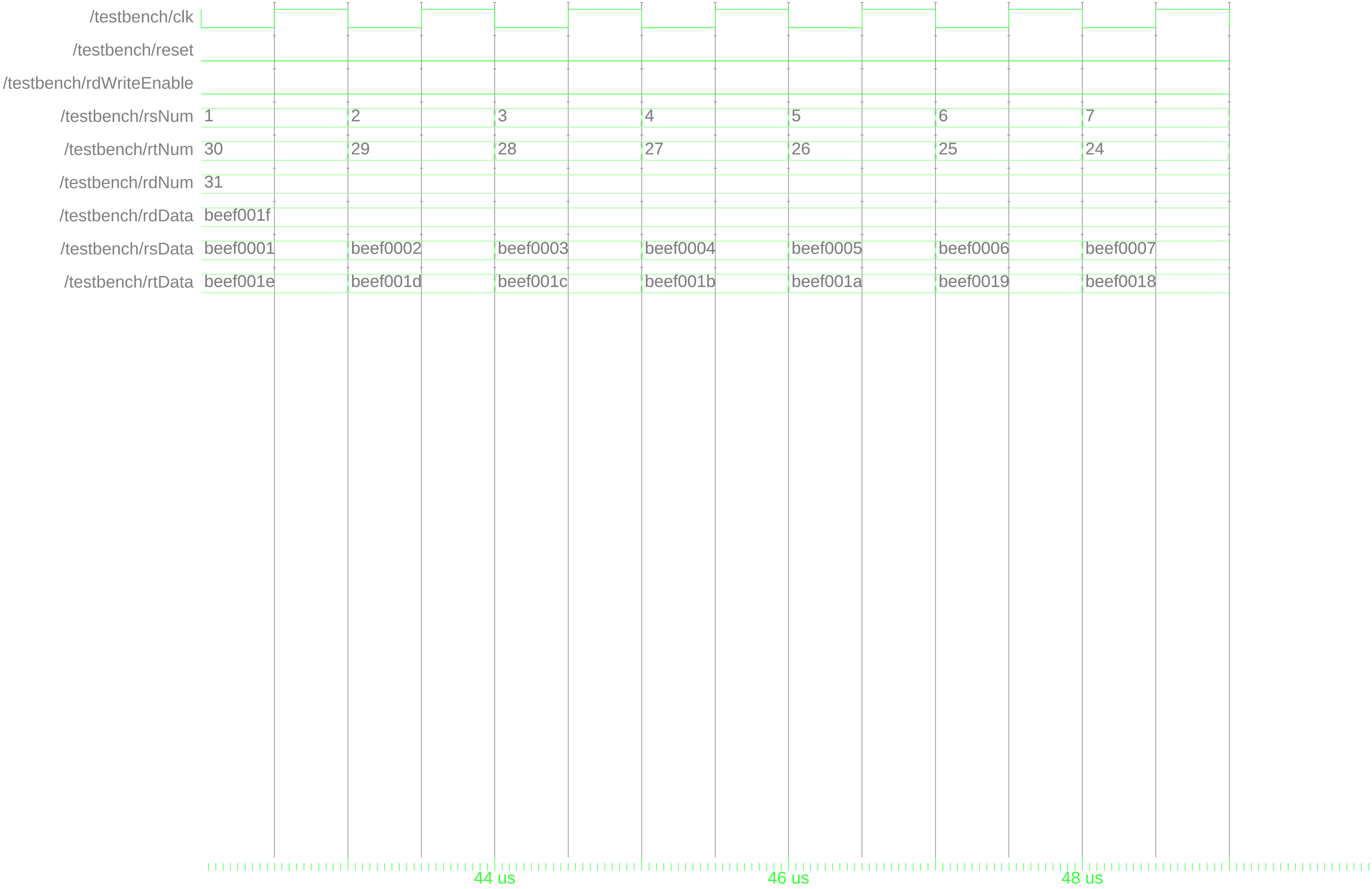
Entity:testbench Architecture: Date: Sun Sep 14 16:48:46 EDT 2003 Row: 4 Page: 4

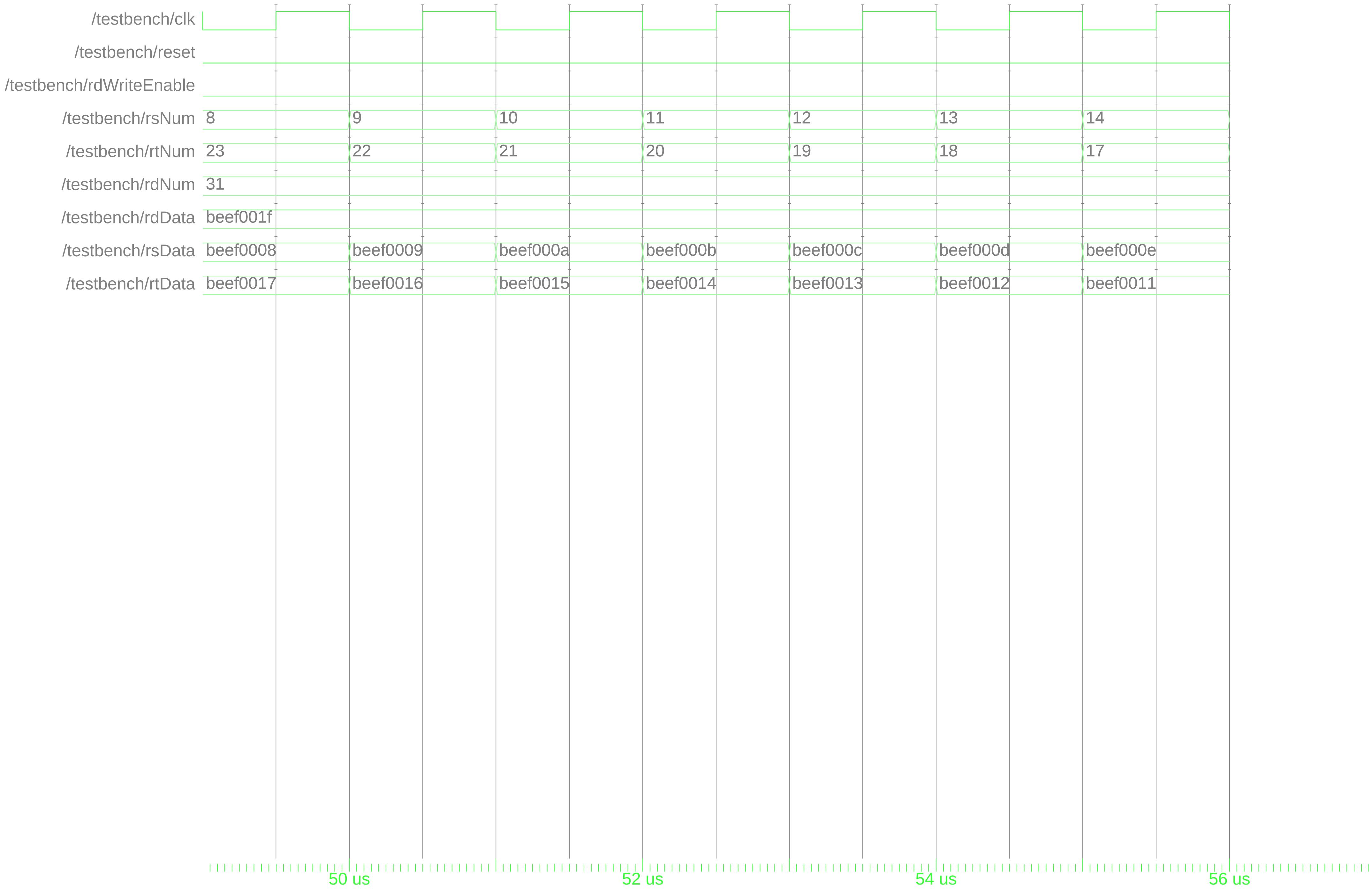


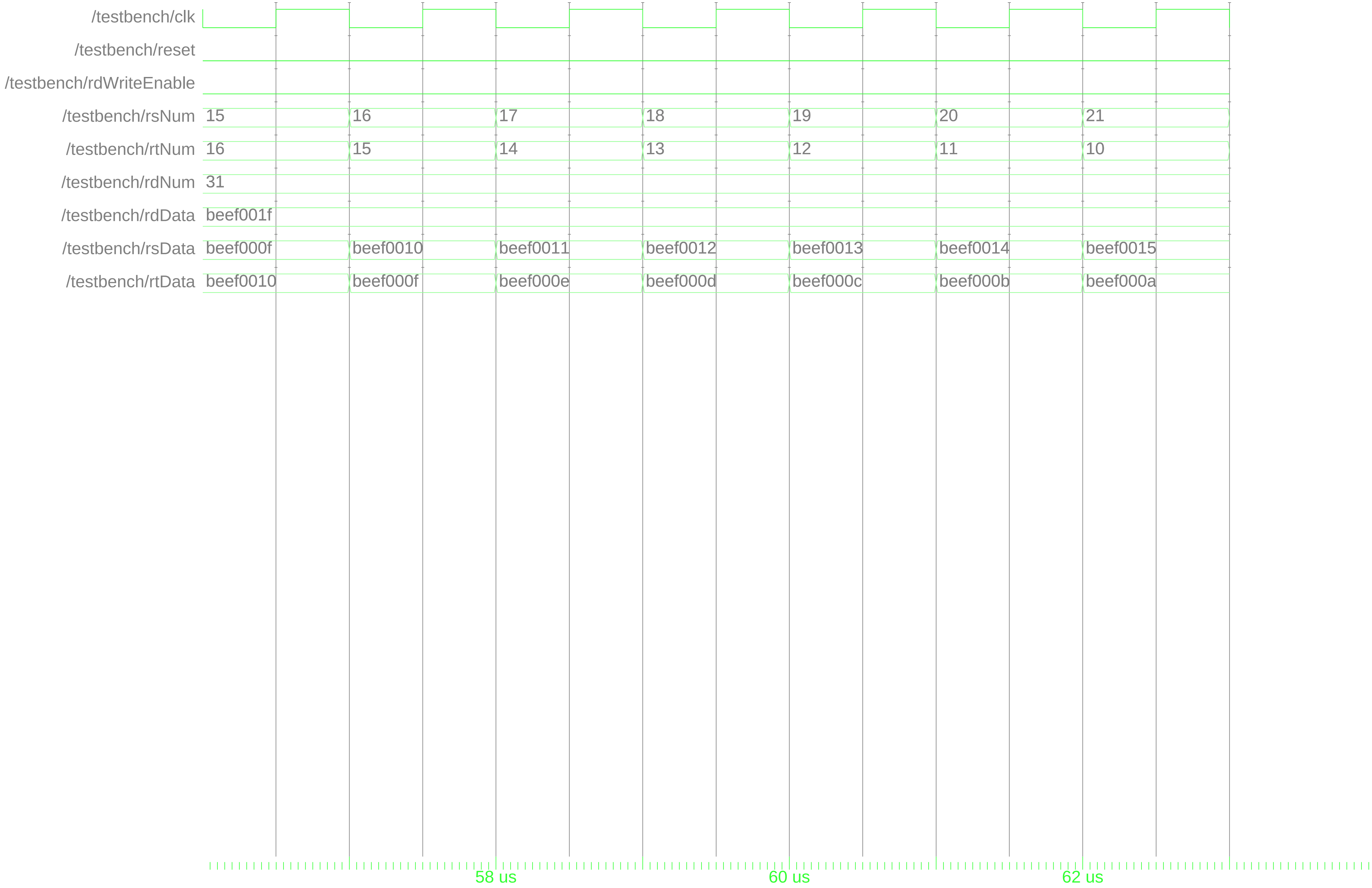
/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
/testbench/rtNum
/testbench/rdNum
/testbench/rdData
/testbench/rsData
/testbench/rtData
21 22 23 24 25 26 27
10 9 8 7 6 5 4
21 22 23 24 25 26 27
beef0015 beef0016 beef0017 beef0018 beef0019 beef001a beef001b
00000000 beef0015 00000000 beef0016 00000000 beef0017 00000000 beef0018 00000000 beef0019 00000000 beef001a 00000000 beef001b
beef000a beef0009 beef0008 beef0007 beef0006 beef0005 beef0004
30 us
32 us
34 us

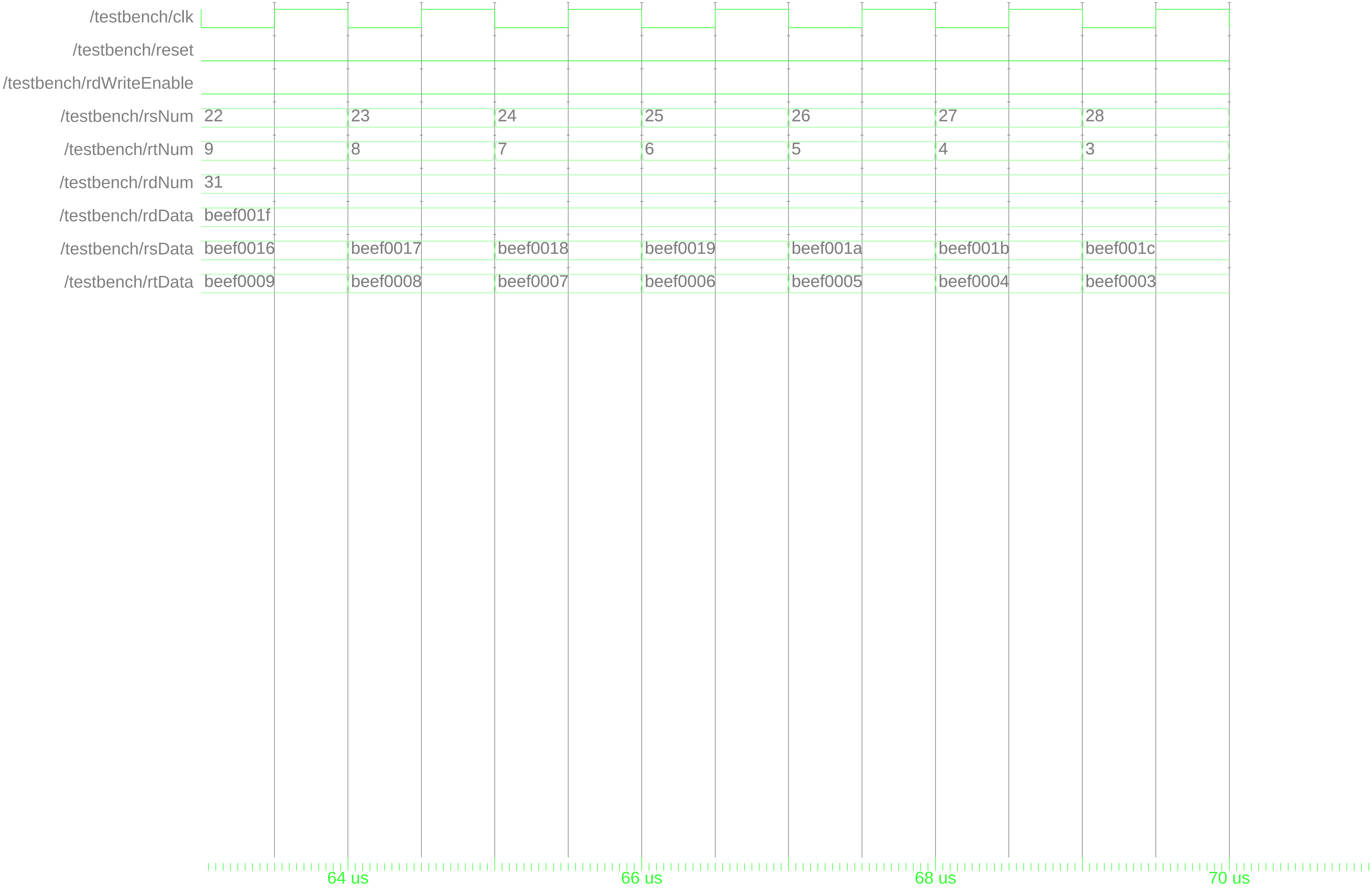


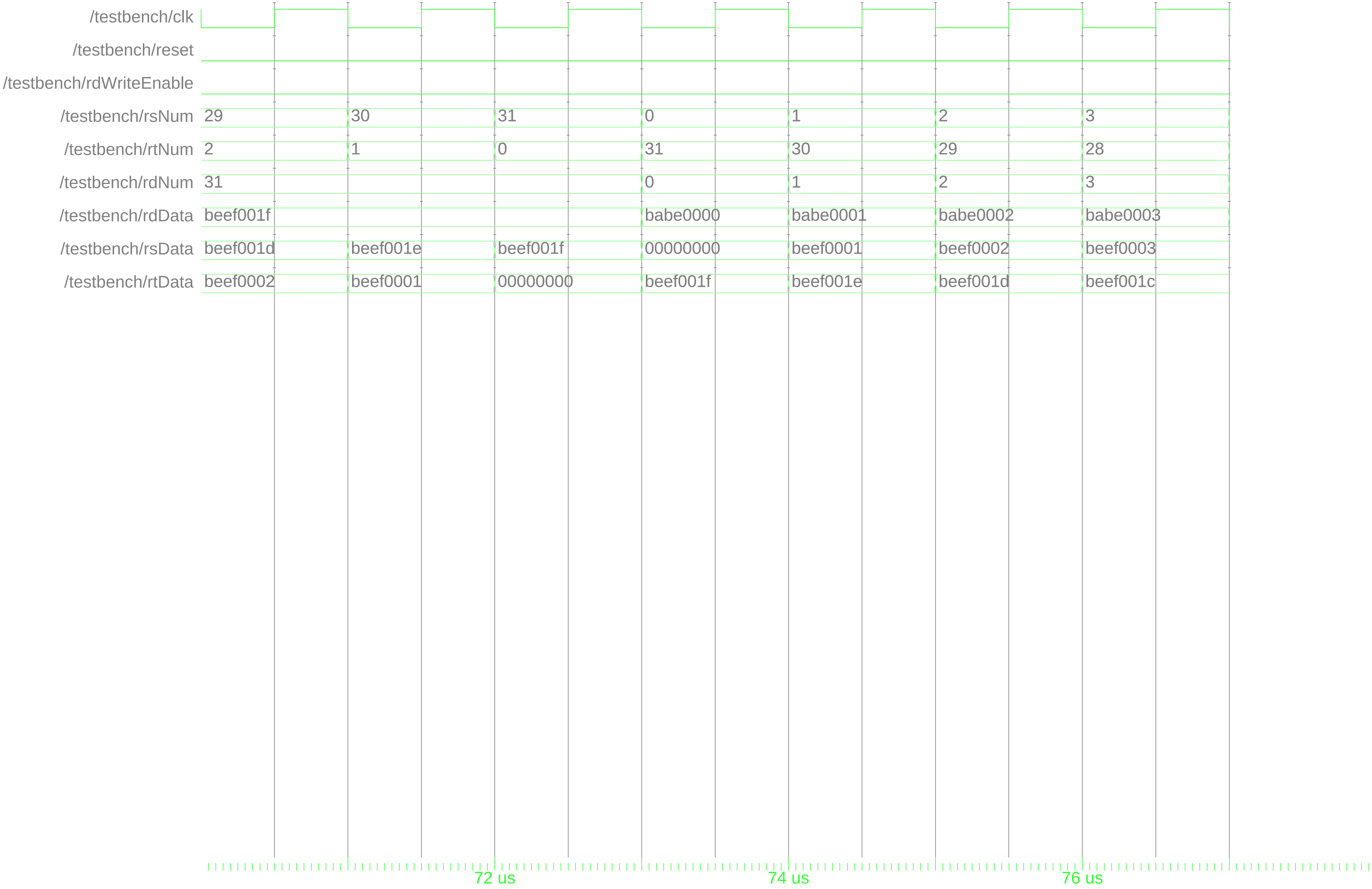
Entity:testbench Architecture: Date: Sun Sep 14 16:48:46 EDT 2003 Row: 6 Page: 6

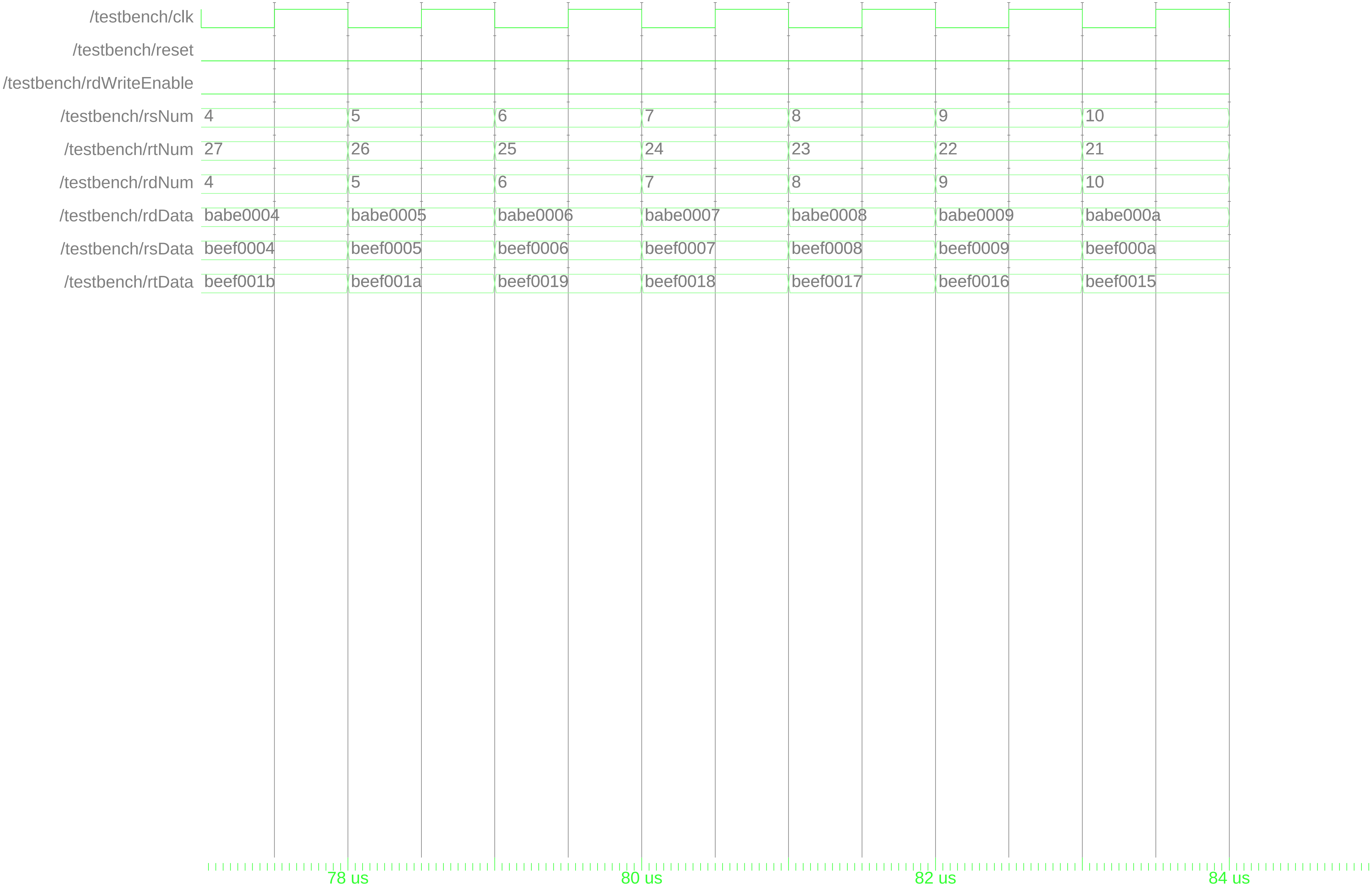

/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
1
2
3
4
5
6
7
/testbench/rtNum
30
29
28
27
26
25
24
/testbench/rdNum
31
/testbench/rdData
beef001f
/testbench/rsData
beef0001
beef0002
beef0003
beef0004
beef0005
beef0006
beef0007
/testbench/rtData
beef001e
beef001d
beef001c
beef001b
beef001a
beef0019
beef0018
44 us
46 us
48 us


/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
8
9
10
11
12
13
14
/testbench/rtNum
23
22
21
20
19
18
17
/testbench/rdNum
31
/testbench/rdData
beef001f
/testbench/rsData
beef0008
beef0009
beef000a
beef000b
beef000c
beef000d
beef000e
/testbench/rtData
beef0017
beef0016
beef0015
beef0014
beef0013
beef0012
beef0011
50 us
52 us
54 us
56 us

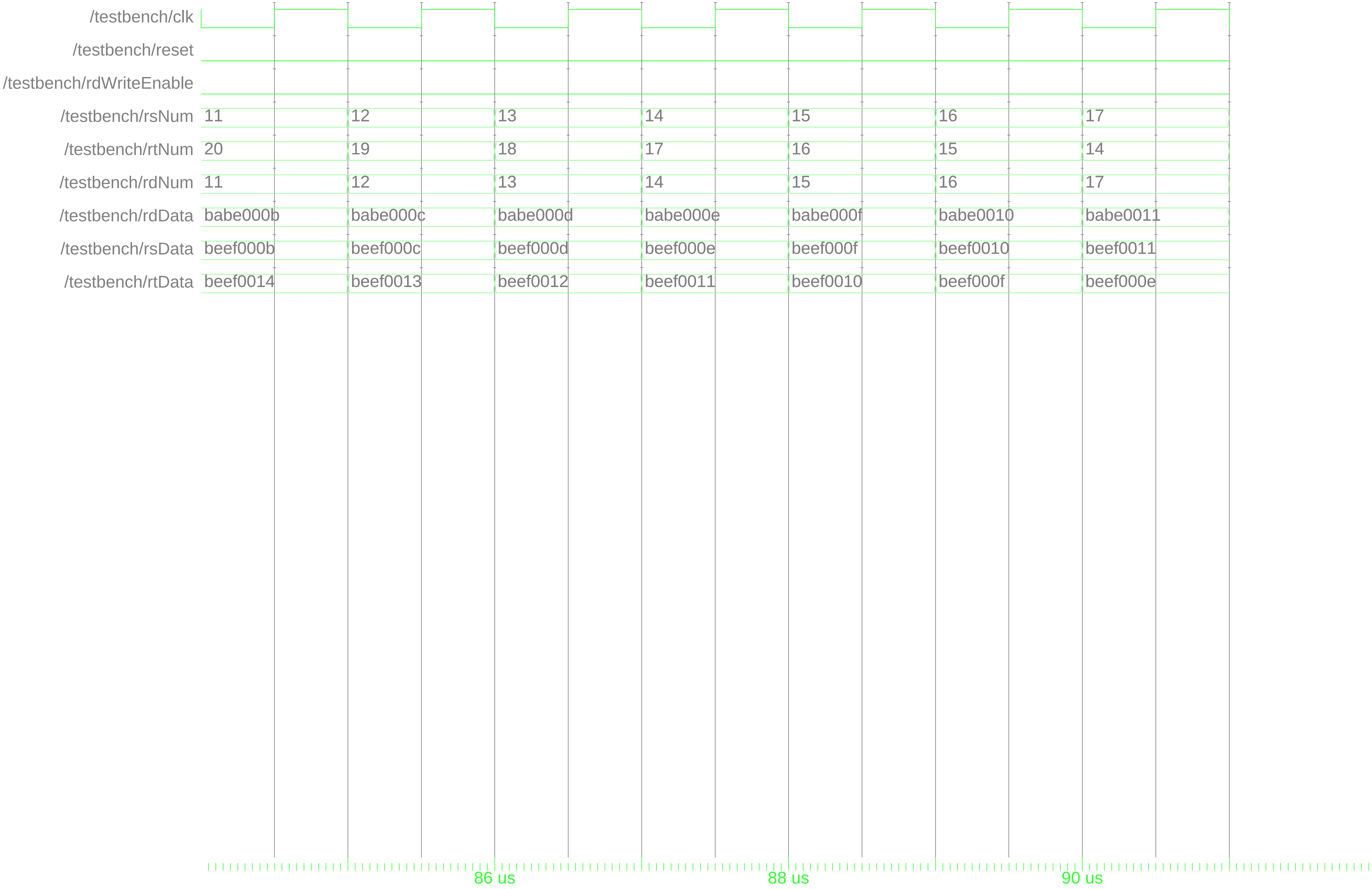

/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
15
16
17
18
19
20
21
/testbench/rtNum
16
15
14
13
12
11
10
/testbench/rdNum
31
/testbench/rdData
beef001f
/testbench/rsData
beef000f
beef0010
beef0011
beef0012
beef0013
beef0014
beef0015
/testbench/rtData
beef0010
beef000f
beef000e
beef000d
beef000c
beef000b
beef000a
58 us
60 us
62 us

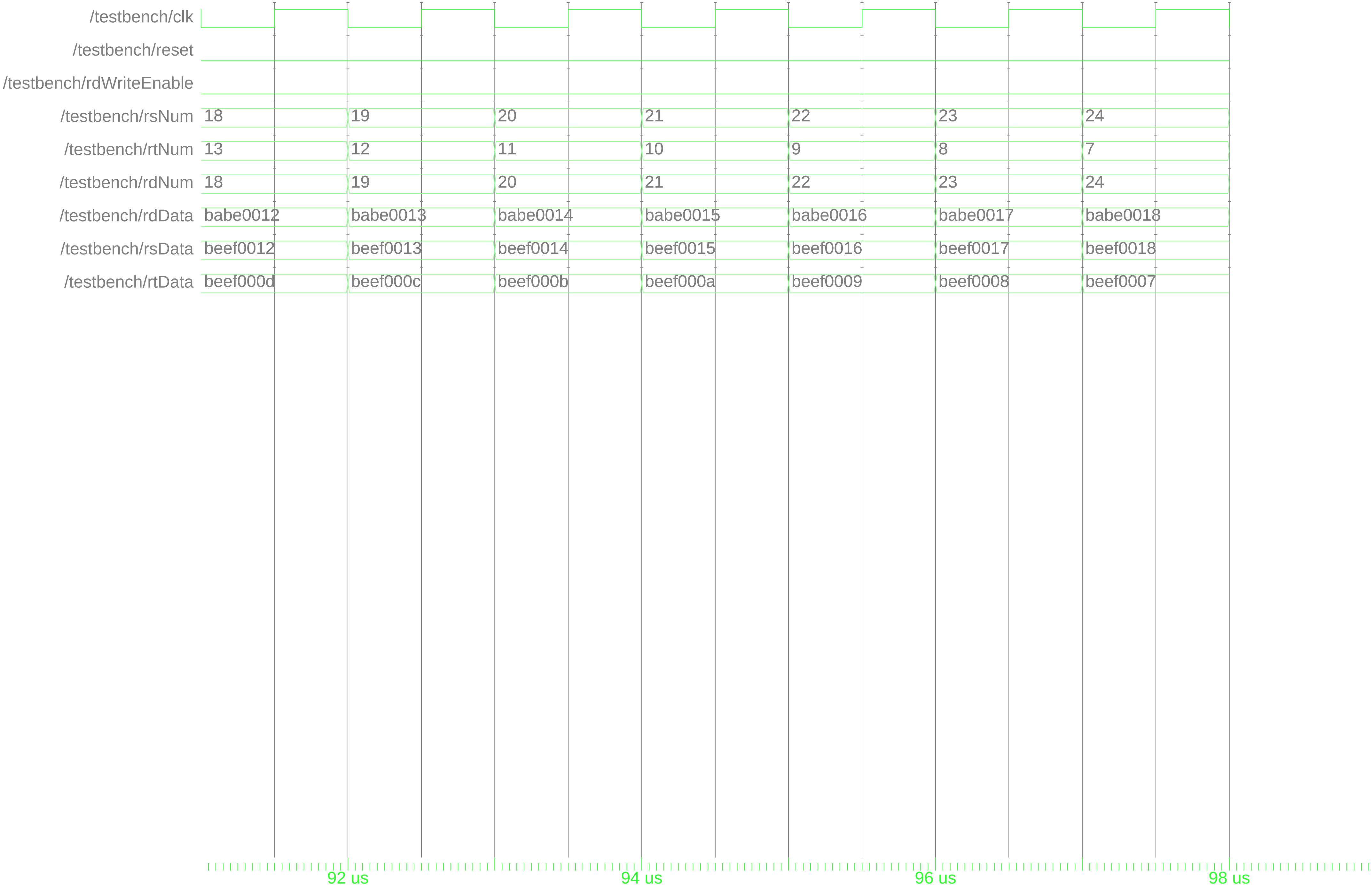

/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
/testbench/rtNum
/testbench/rdNum
/testbench/rdData
/testbench/rsData
/testbench/rtData
22
23
24
25
26
27
28
9
8
7
6
5
4
3
31
beef001f
beef0016
beef0017
beef0018
beef0019
beef001a
beef001b
beef001c
beef0009
beef0008
beef0007
beef0006
beef0005
beef0004
beef0003
64 us
66 us
68 us
70 us

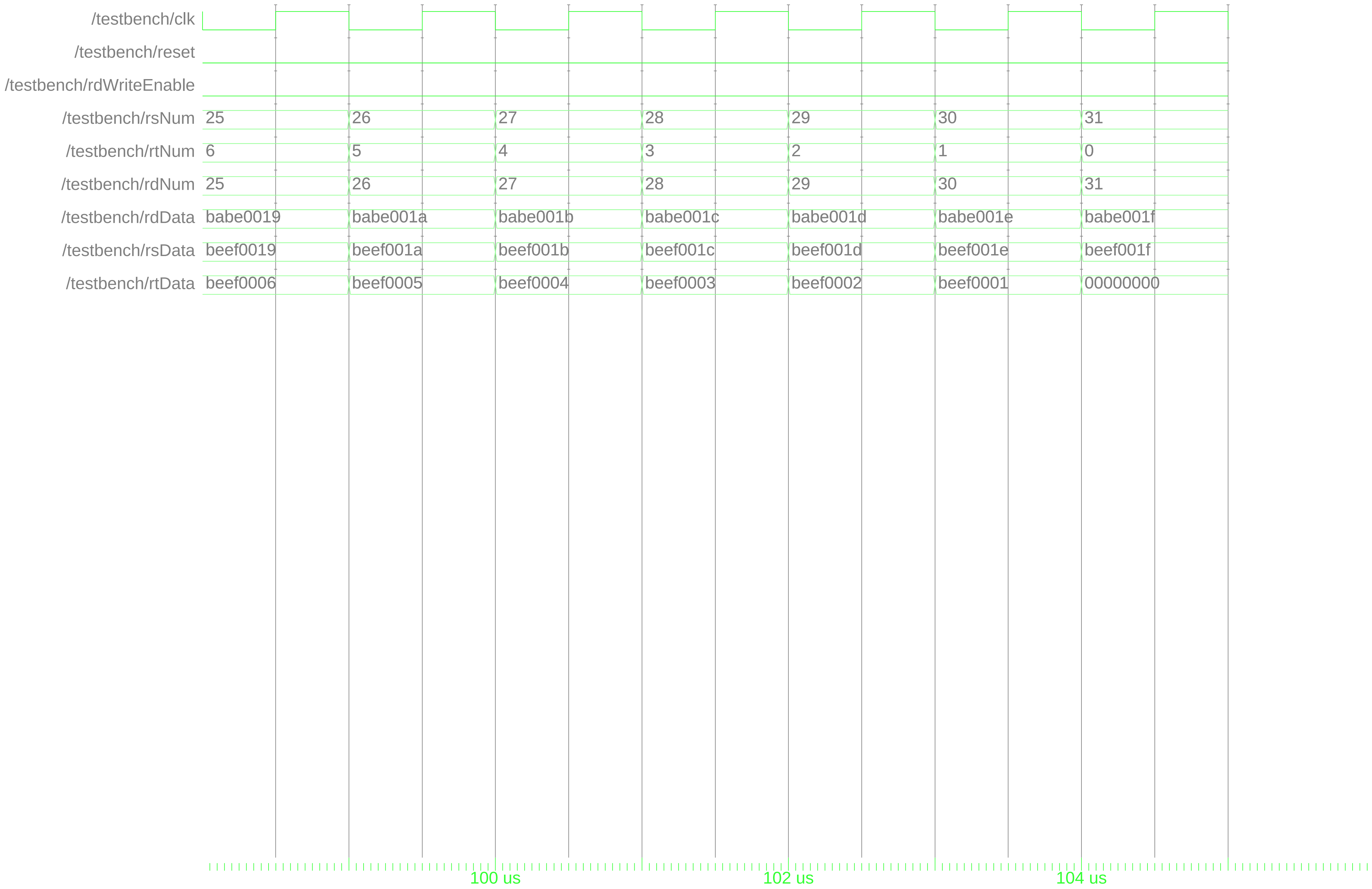

/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum 29 30 31 0 1 2 3
/testbench/rtNum 2 1 0 31 30 29 28
/testbench/rdNum 31 0 1 2 3
/testbench/rdData beef001f babe0000 babe0001 babe0002 babe0003
/testbench/rsData beef001d beef001e beef001f 00000000 beef0001 beef0002 beef0003
/testbench/rtData beef0002 beef0001 00000000 beef001f beef001e beef001d beef001c
72 us
74 us
76 us



/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
/testbench/rtNum
/testbench/rdNum
/testbench/rdData
/testbench/rsData
/testbench/rtData
4
5
6
7
8
9
10
27
26
25
24
23
22
21
4
5
6
7
8
9
10
babe0004
babe0005
babe0006
babe0007
babe0008
babe0009
babe000a
beef0004
beef0005
beef0006
beef0007
beef0008
beef0009
beef000a
beef001b
beef001a
beef0019
beef0018
beef0017
beef0016
beef0015
78 us
80 us
82 us
84 us


/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
/testbench/rtNum
/testbench/rdNum
/testbench/rdData
/testbench/rsData
/testbench/rtData
11 12 13 14 15 16 17
20 19 18 17 16 15 14
11 12 13 14 15 16 17
babe000b babe000c babe000d babe000e babe000f babe0010 babe0011
beef000b beef000c beef000d beef000e beef000f beef0010 beef0011
beef0014 beef0013 beef0012 beef0011 beef0010 beef000f beef000e
86 us
88 us
90 us


/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum 18 19 20 21 22 23 24
/testbench/rtNum 13 12 11 10 9 8 7
/testbench/rdNum 18 19 20 21 22 23 24
/testbench/rdData babe0012 babe0013 babe0014 babe0015 babe0016 babe0017 babe0018
/testbench/rsData beef0012 beef0013 beef0014 beef0015 beef0016 beef0017 beef0018
/testbench/rtData beef000d beef000c beef000b beef000a beef0009 beef0008 beef0007
92 us
94 us
96 us
98 us


/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
/testbench/rtNum
/testbench/rdNum
/testbench/rdData
/testbench/rsData
/testbench/rtData
25 26 27 28 29 30 31
6 5 4 3 2 1 0
25 26 27 28 29 30 31
babe0019 babe001a babe001b babe001c babe001d babe001e babe001f
beef0019 beef001a beef001b beef001c beef001d beef001e beef001f
beef0006 beef0005 beef0004 beef0003 beef0002 beef0001 00000000
100 us
102 us
104 us


/testbench/clk
/testbench/reset
/testbench/rdWriteEnable
/testbench/rsNum
31
/testbench/rtNum
0
/testbench/rdNum
31
/testbench/rdData
babe001f
/testbench/rsData
beef001f
/testbench/rtData
00000000
106 us
108 us
110 us
112 us